

### **REMARKS**


Claims 30-39 are in the application for consideration, and are allowed.

The title is incorrect in the Notice of Allowance dated June 23, 2004. The title was previously amended to be consistent with the claimed subject matter in an amendment filed by Applicant on April 19, 2004. It is respectfully requested that the title be amended as noted.

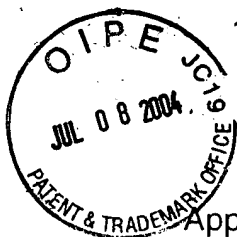
There remains a PTO Form-1449 properly submitted by Applicant which the undersigned has yet to receive an initialed copy from the Examiner. Applicant submitted an Information Disclosure Statement on February 26, 2004. A duplicate copy of that Information Disclosure Statement, including the PTO Form-1449 and a copy of the return receipt postcard, Express Mail Receipt, and the Ex-Post Declaration are enclosed. It is respectfully requested that the Examiner initial the enclosed Form-1449 and provide the undersigned with a copy of the initialed PTO Form-1449.

Respectfully submitted,

Dated: July 8, 2004

By:   
Deepak Malhotra  
Reg. No. 33,560

EL 979954244



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 10/713,647  
 Filing Date ..... November 13, 2003  
 Inventor ..... Luan C. Tran  
 Assignee ..... Micron Technology, Inc.  
 Group Art Unit ..... 2818  
 Examiner ..... Unassigned  
 Attorney's Docket No. .... MI22-2429  
 Title: 6F<sup>2</sup> DRAM Array, A DRAM Array Formed on a Semiconductive Substrate,  
 A Method of Forming Memory Cells in a 6F<sup>2</sup> DRAM Array and a Method  
 of Isolating a Single Row of Memory Cells in a 6F<sup>2</sup> DRAM Array

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

References -See Attached Form PTO-1449

The Examiner's attention is directed to the reference which is listed on the attached Form PTO-1449. No admission is made regarding whether the submitted reference is prior art.

This Supplemental Information Disclosure Statement is being filed before the mailing of a first Office Action on the merits. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. Section 1.17(p) to Deposit Account No. 23-0925.

Citation of the referenced art is respectfully requested.

**COPY**

Respectfully submitted,

Dated: Feb. 26, 2004

By:   
 Deepak Malhotra  
 Reg. No. 33,560

EV 372457990

EL 979 154244

Sheet 1 of 1

Form PTO-1449

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.  
M122-2429SERIAL NO.  
10/713,647LIST OF ART CITED BY APPLICANT  
(Use several sheets if necessary)APPLICANT  
Luan C. TranFILING DATE  
November 13, 2003GROUP  
Priority # 2818

## U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	5,693,971	12/1997	GONZALEZ	257	314	
	AB	6,077,742	06/2000	CHEN ET AL.	438	255	
	AC	6,294,436 B1	09/2001	PARK ET AL.	438	396	
	AD	6,590,817 B2	07/2003	SIEK	365	201	
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

## FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

	AR		
	AS		
	AT		

EXAMINER

DATE CONSIDERED

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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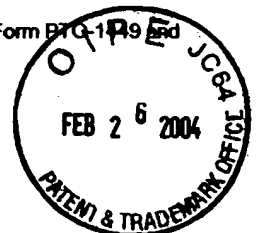
**EL 979954244**

MI22-2429  
Micron Technology, Inc.

February 26, 2004



The stamp of the U.S. Patent and Trademark Office acknowledges receipt of the following papers relating to the Patent application of Luan ~~Tran~~, Serial No. 10/713,647, filed November 13, 2003, entitled "A 6F<sup>2</sup> DRAM Array, a DRAM Array Formed on a Semiconductive Substrate, a Method of Forming Memory Cells in a 6F<sup>2</sup> DRAM Array, and a Method of Isolating a Single Row of Memory Cells in a 6F<sup>2</sup> DRAM Array"

1. PTO Return Postcard Receipt
2. Transmittal Form (PTO/SB/21)
3. Supplemental Information Disclosure Statement, including Form PTO-149 and cited references



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E 979954244

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION SERIAL NO ..... 10/713,647  
CONFIRMATION NO. .... 1643  
FILING DATE ..... November 13, 2003  
INVENTORSHIP ..... Luan C. Tran  
ASSIGNEE ..... Micron Technology, Inc.  
GROUP ART UNIT ..... 2818  
EXAMINER ..... Unassigned  
CUSTOMER NO. .... 021567  
ATTORNEY'S DOCKET NO ..... MI22-2429  
TITLE: A 6F<sup>2</sup> DRAM Array, a DRAM Array Formed on a Semiconductive Substrate, a Method of  
Forming Memory Cells in a 6F<sup>2</sup> DRAM Array, and a Method of Isolating a Single Row of  
Memory Cells in a 6F<sup>2</sup> DRAM Array

EX POST DECLARATION OF EXPRESS MAILING

I hereby declare that I have deposited the below-listed papers with the United States Postal Service "Express Mail Post Office to Addressee" service, addressed and posted as indicated below, in compliance with 37 CFR 1.10. The Express Mail label No. has been placed on one or more of the papers listed.

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Alexandria, VA 22313-1450

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Listing Of Items Enclosed in Mailing:

1. PTO Return Receipt Postcard
2. Transmittal Form (PTO/SB/21)
3. Information Disclosure Statement including PTO Form-1449 and cited references

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful statement may jeopardize the validity of the application or any patent issued therefrom.

Dated at Spokane, Washington this 26th day of February, 2004.

Trinity Coxon  
Printed Name: Trinity Coxon

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